EmbedSanitizer: Tool for Detecting Races in 32-bit ARM Applications

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1 Introduction
Multicore embedded systems are everywhere: smart phones, TVs, IoT. Thus, writing concurrent programs for these platforms is common. Unfortunately, it is easy to introduce data races in such applications during implementation. We propose a tool for detecting races in target embedded systems because of limitations in the literature:

- Available tools are architecture specific, e.g; ThreadSanitizer for 64-bit systems [1].
- Detecting races on another architecture may need emulation of some hardware specific features.
- Race detection through emulation is slow.

2 Contributions
- EmbedSanitizer [3], a tool for detecting races in programs for 32-bit ARM.
- We motivate the idea of detecting races in target embedded systems and show usability.
- We evaluate our tool and show applicability using PARSEC benchmarks on a smart TV with ARMv7 CPU.

3 Motivation

```c
# VideoSignalQueue queue;
void ReceiveThread() {
  while(true) {
    Signal s = receive();
    queue.put(s);
  }
}
void DisplayThread() {
  while(true) {
    Signal s = queue.get();
    display(s);
    record(s);
  }
}
```

Figure 1: A theoretical example with 3 threads concurrently accessing a shared queue while interacting with hardware peripherals: antenna, screen, and hard-disk.

We promote utilization of existing race detection tools for embedded system architectures. The challenge is how to detect races while threads interact with peripherals, as in program on Figure 1. In the literature, the programmer has to emulate the hardware peripherals and then analyze the program against races on an alternative architecture or slow emulator. However, the use of target hardware for race detection improves detection precision and runtime performance.

4 Methods
Our tool is implemented as part of LLVM/Clang, in similar form to ThreadSanitizer, as shown on figure 2.

1. Modify the LLVM/Clang compiler arguments parser to support instrumentation of 32-bit ARM programs.
2. Implement instrumentation pass in to identify synchronization events and memory accesses.
4. Cross-compile your 32-ARM program using Clang and execute the binary in a target hardware.

Figure 2: High level overview of ThreadSanitizer and EmbedSanitizer in LLVM/Clang. In (a) ThreadSanitizer: essential LLVM modules for race detection. In (b) EmbedSanitizer: same modules we enhanced to instrument and detect races for 32-bit ARM.

5 Evaluation

Precision Evaluation
Table 1: Experimental results to compare race detection in ARMv7 using EmbedSanitizer vs in x86_64 with ThreadSanitizer.

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Input</th>
<th>Data races</th>
<th>Anticalls</th>
<th>Reads</th>
<th>Writes</th>
<th>Time</th>
<th>Ours</th>
<th>Precision</th>
<th>slowdown</th>
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</tbody>
</table>

Performance Evaluation

Figure 3: Slowdown comparison of race detection on ARMv7 vs on Qemu-ARM.

6 Conclusions
- We propose EmbedSanitizer, a tool for detecting races in 32-bit ARM applications.
- We use 4 PARSEC applications to evaluate its precision.
- We compare its slowdown on Qemu vs on target hardware.

7 Future Work
- Improving the efficiency of race detection runtime by using a hybrid of race detection algorithms.
- Supporting other 32-bit architectures.
- Evaluating with real-world applications which use features of the embedded systems such as sensors and actuators – a real motivation for our work.

8 References